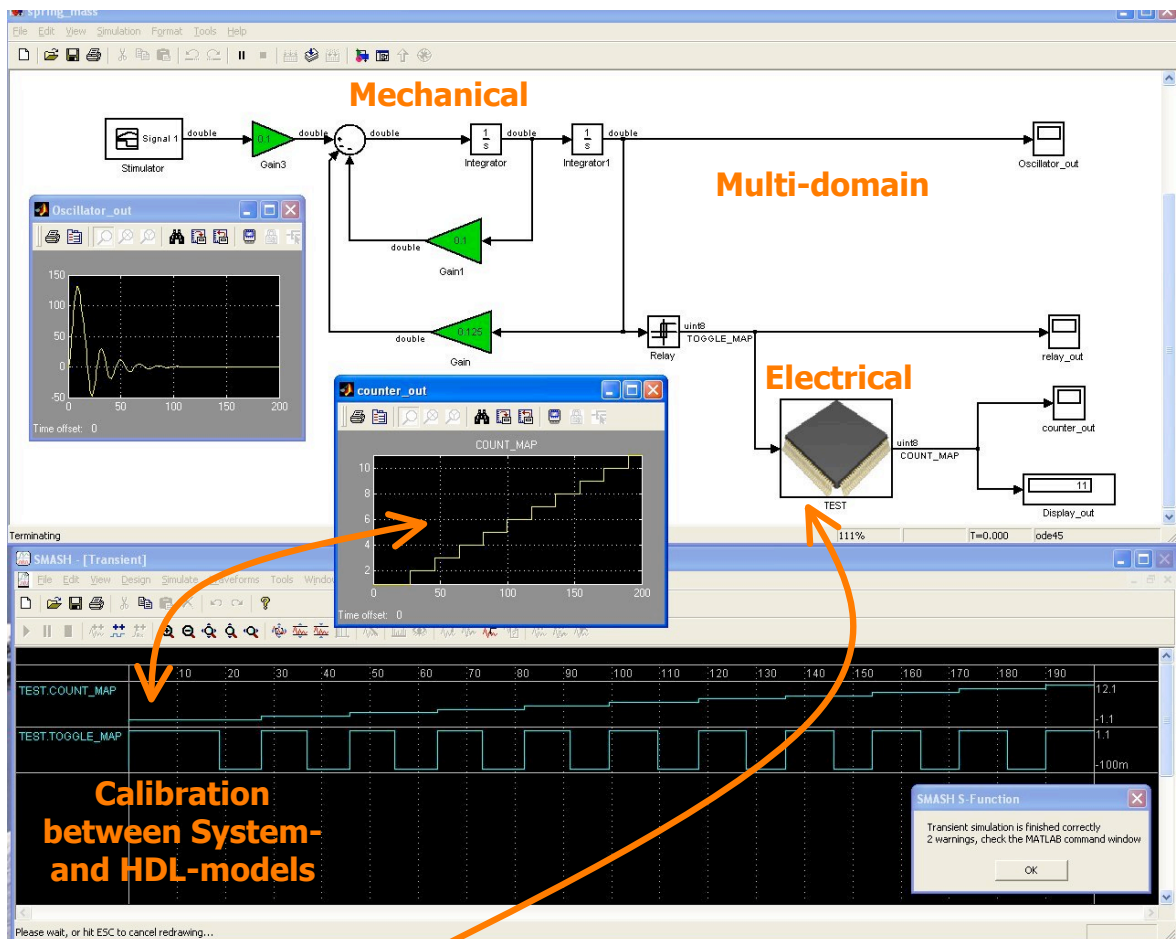


**Bridge the gap between System-level and HDL-level thanks to SMASH/Simulink already available in SMASH 5.2 !**

### Problems addressed:

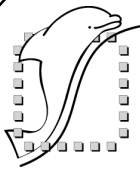
- Complete mixed-signal top-down & bottom-up simulation
- System specification
- Design gap between system-level and HDL-level designs
- Calibration of models at any hierarchical level
- Multi-domain design



**Direct instantiation of the HDL-block in Simulink**

**Simulink & SMASH co-simulation results**

From Application Note V10 available on request



- **Complete mixed-signal top-down & bottom-up capabilities to accelerate circuit development and verification !**

- **Combining powerful capabilities of Matlab/Simulink for model-based and system-level design with those of SMASH for top-down mixed-language, multi-level and multi-domain design.**

- **SMASH/Simulink fills the gap** between system-level design and HDL design by :

- ✓ giving the ability to validate the whole system's behavior and then the initial high level specification .

- ✓ enabling to bring any HDL block back into the system co-simulation in order to validate the behavior of each block in the Matlab system. This way, you increase your calibration level by ensuring HDL and system-level models are equivalent.

- ✓ allowing multi-domain design e.g. electric and mechanic domains.

- ✓ simplifying the design and simulation of the behavior of complex systems including both micro- and macro- blocks.

- ✓ allowing faster Time-to-Market thanks to earlier adaptations during System-level to HDL designed phase and easier re-usability of your designed blocks.

- **Already available in SMASH 5.2 :**

Logic connection (Verilog, VHDL), Windows version.

- **And Soon...**

In the Spring 2004, concerning Matlab/Simulink co-simulation, SMASH 5.3 will be comprized of :

- Analog connection (SPICE, VHDL-AMS)

- UNIX versions

